

Serial No. 09/922,950  
Docket No.: A305-1  
AMA.040

**AMENDMENT TO THE SPECIFICATION**

The paragraph commencing at page 7, line 13 is amended as follows:

The microcomputer 1 is comprised of a central processing unit (CPU) 2, a bus controller 4 electrically connected to the CPU 2 through a data bus 3, a command cache 7 electrically connected to the CPU 2 through a command bus 5, and to the bus controller 4 through a first connection bus 6, a memory controller 9 electrically connected to the bus controller 4 through an internal bus 8, a command random access memory (RAM) 12 electrically connected to the command bus 5 through a second connection bus 10, and to the internal bus 8 through a third connection bus 11, and an external terminal 13 electrically connected to the CPU 2.